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If nothing happens, download GitHub Desktop and try again. This is the standard project that is generated by Xilinx Platform Studio's Base System Builder for the ZC706 evaluation board. It doesn't ...

[Project created with the Base System Builder for the ZC706](#)

The data capture platform interfaces to the RadioVerse evaluation boards through a variety of interfacing standards ... Evaluation or Prototyping Software Packages. Comparison of ZC706 and ...

[EVAL-TPG-ZYNQ3](#)

The 16 DAC8728s will be interfacing to a Xilinx ZC706 evaluation board for the digital interface and then likely to our our FPGA. I am just getting started but would like to start communicating with a ...

[DAC8728: DAC8728 Multi-Channel Design](#)

It can be build for Xilinx Zynq FPGA. A SD-Card image for the

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ZC706 evaluation board is included, containing the implementation and a petalinux build. This project is part of a bachelor thesis in ...

[sea212 / Implementation-of-an-artificial-neural-network-on-a-zynq7045-fpga-using-sdsoc](#)

Target Platform selections include Xilinx Evaluation Boards, Altera Evaluation Boards, or the FMCOMMS2/3 ZC706 SDR Platform. The rest of the code generation and target integration process can then be ...

[Four Quick Steps to Production: Using Model-Based Design for Software-Defined Radio \(Part 4\)](#)

with ADC drivers and clock distribution circuits integrated on board. The Xilinx ZC706 FPGA development board is used as a DAQ board. The data from the ADC are routed to the FPGA through the FMC (FPGA ...

[Development of an ADC radiation tolerance characterization system for the upgrade of the ATLAS LAr calorimeter](#)

The DAC mode is Single (DAC A), 1 IQ Pair with 6x interpolation. I've attached a file which has the screen shots from the evaluation board equivalent settings. The actual messages I send are as ...

[DAC38RF82: Unable to generate signal from DAC: single tone only.](#)

Based on the TID performance, we have designed a SEE evaluation system for ADS5272 ... ADS5272 was chosen to be implemented in the full-size LAr Trigger Digitizer Board (LTDB) demonstrator, which will ...

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